

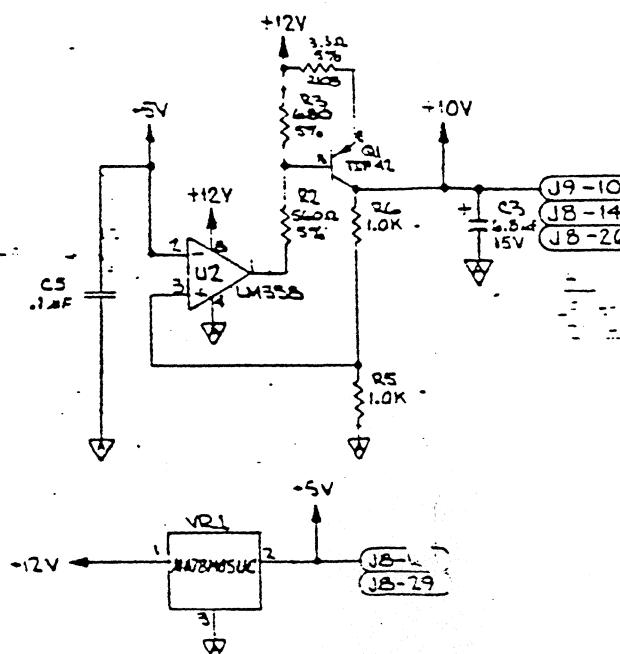
TYPE	REF. DESIGNATION	+5V	-5V	AGND	+12V	-12V	GND	10V	SPARE
74L500	U4	14			7			4.A.	
74L503	U9	14			7			4.A.	
74L504	U10	14			7			3.5-1.2	
7406	U23, U28, U31	14			7			U25-2.4	J31-2
74L514	U5, U9, U3	14			7			U3-2.4	U37-6
7438	U11, U30	14			7			U30-6	
74L574	U8, U2, U42, U22	14			7			U5-9	J22-9
74574	U22	14			7			4.A.	
74586	U15	14			7			4.A.	
74L5123	U25, U16	16			8			4.A.	
74S188	U7	16			8			4.A.	
5051	U6	40			20			4.A.	
FFA	U2	20			10			4.A.	
75452	U21, U29	5			4			4.A.	
26L534	U41	16			8			U41-7, 9, 15	
26L532	U35	16			8			U35-3, 5, 13	
10124	U39	16	8		9			U39-7, 10	
K08-8	U3	13		3	1			4.A.	
4558	U1			8	4				
CA3096	U46			16					
CA3046	U34				5				
CA3083	U44								
CA4020	U14			16			8		4.A.

Diagram illustrating the connection between J12 and J2 pins:

- J12-11 → J2-10
- J12-3 → J2-10
- J12-6 → J2-3
- J12-7 → J2-5
- J12-10 → J2-9
- J12-11 → Ground Plane
- J2-7 → C57 (Capacitor)
- J2-7 → Ground Plane

#### POWER DISTRIBUTION

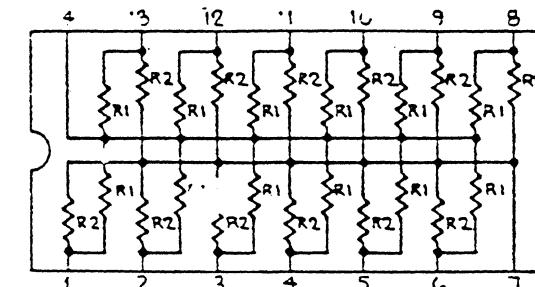
I.C. TABLE		SPARE	
NATION	7EV1+5★AGND+12V+10V10GND+10V		
7	13	↑	



## ON BOARD VOLTAGE REGULATORS

VERTEX CONFIDENTIAL

		DATE	
A	INITIAL RELEASE	3-13-83	225
B	REVISED	3-21-83	PAR
C	REVISED. SEE CHANGE SHEET	5/28/83	PAR
D	REVISED	4/1/83	PAR
E	REVISED	5/30/83	PAR
F	REVISED. SEE CHANGE SHEET	5/30/83	PAR
G	REVISED. SEE CHANGE SHEET	6/6/83	PAR
H	NOT USED		
J	REVISED. PER ECN 0019	7/16/83	215
K	SEE ECN 032	9/9/83	215
L	SEE ECN 071	9/16/83	215
M	SEE ECN 235	9/28/83	205
N	SEE ECN 339	10/11/83	217
P	SEE ECN 362	11/1/83	912
R	SEE ECN 377	11/1/83	917
Q	SEE ECN 374	11/1/83	012



RPI  
R1 = 220  
R2 = 330

7. ALL 1.5 FT CAPACITORS ARE ~~1-28%~~ 25V.

6 ALL 55 RESISTORS ARE 1K

5 ALL DODGES ARE IN 914.

4 ALL RESISTOR VALUES ARE IN OHMS. *Key, 1%*

3 ALL CAPACITOR VALUES ARE IN

## SYMBOLS FOR VOTS

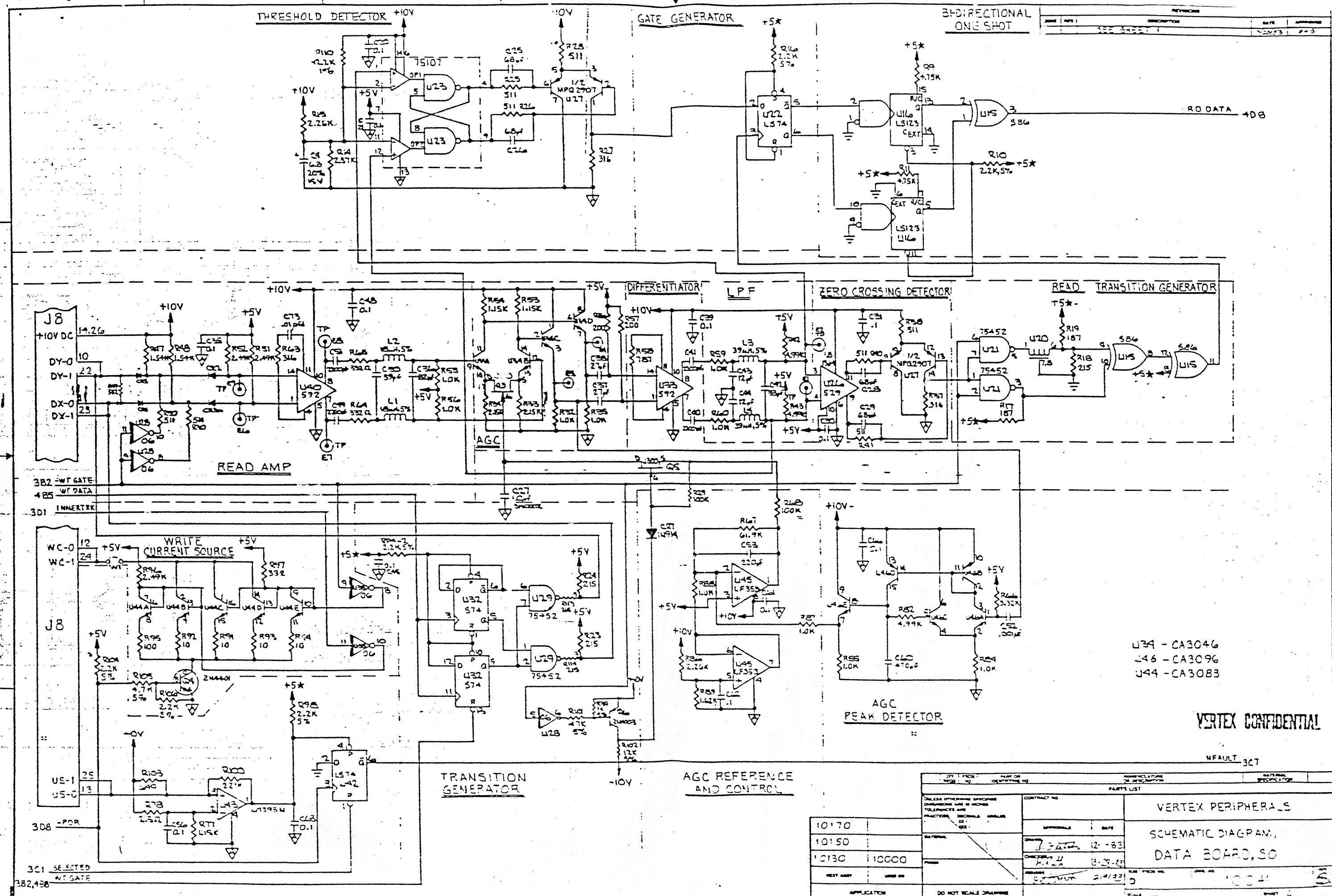
#### 1. SYMBOLS FOR GROUND:

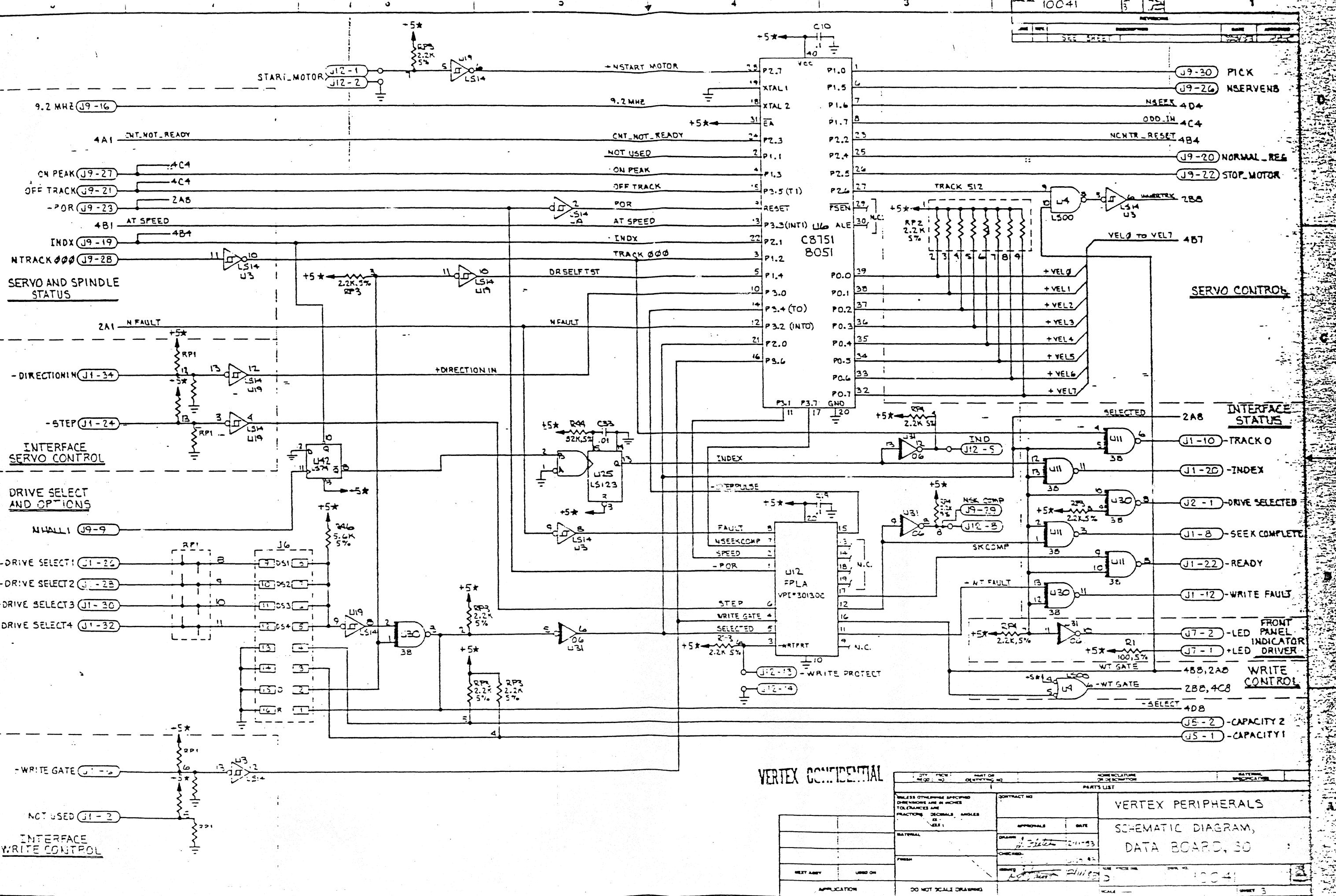
⊕ FRAME GND; Ⓛ ANALOG GND; ± DIGITAL GND

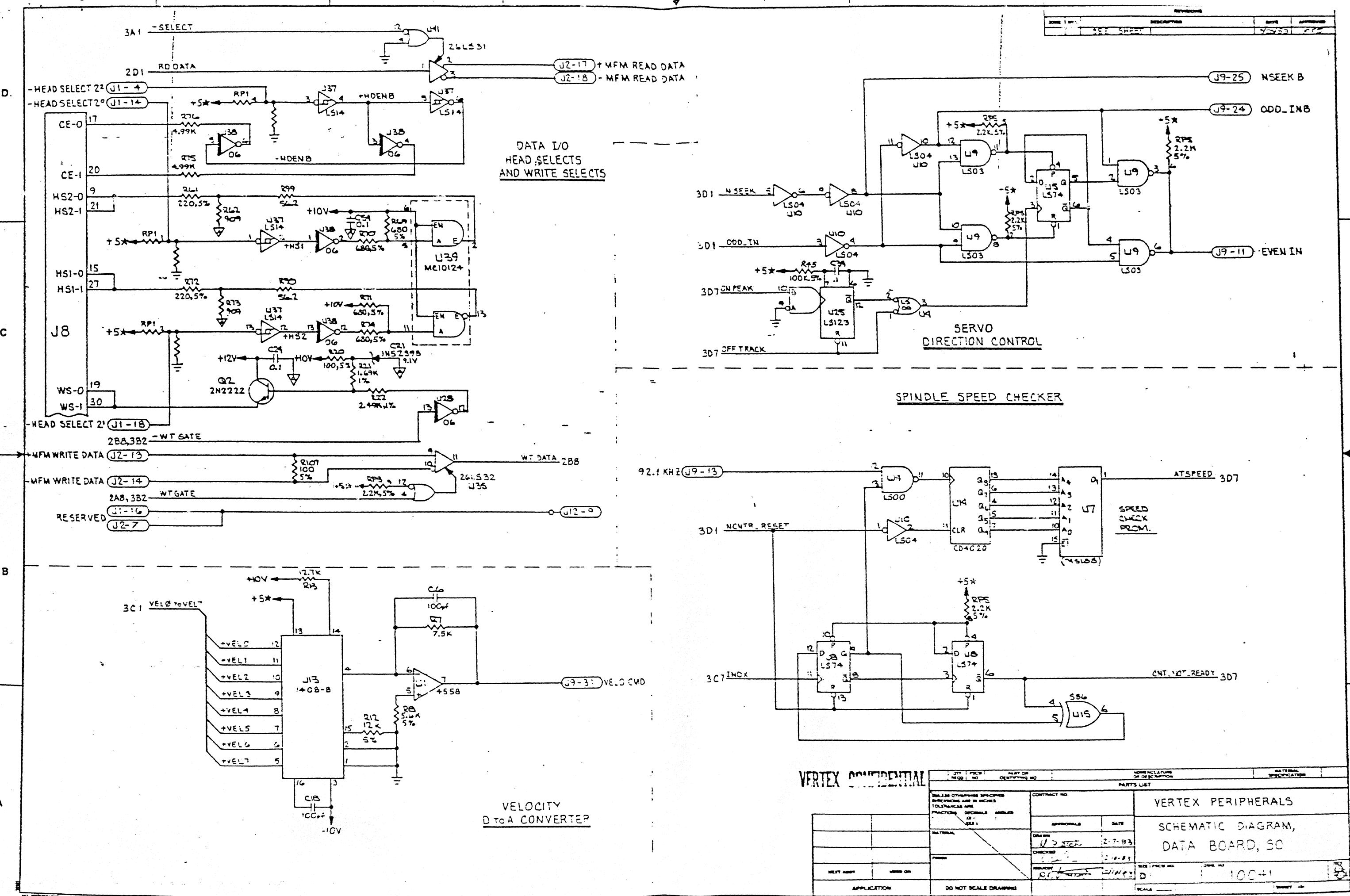
NOTES: UNLESS OTHERWISE SPECIFIED.

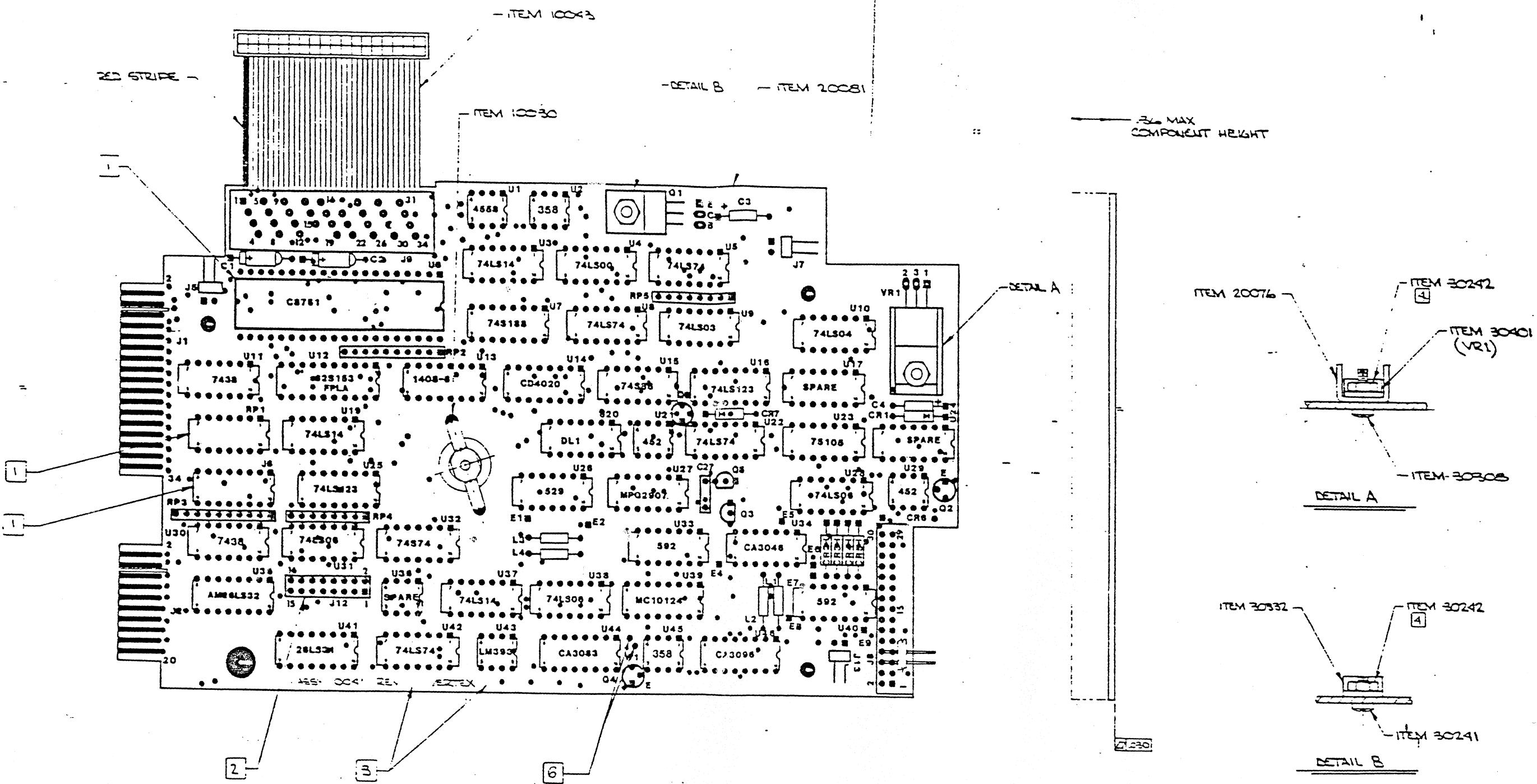
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VERTEX PERIPHERALS  
SCHEMATIC DIAGRAM,  
DATA BOARD, 160







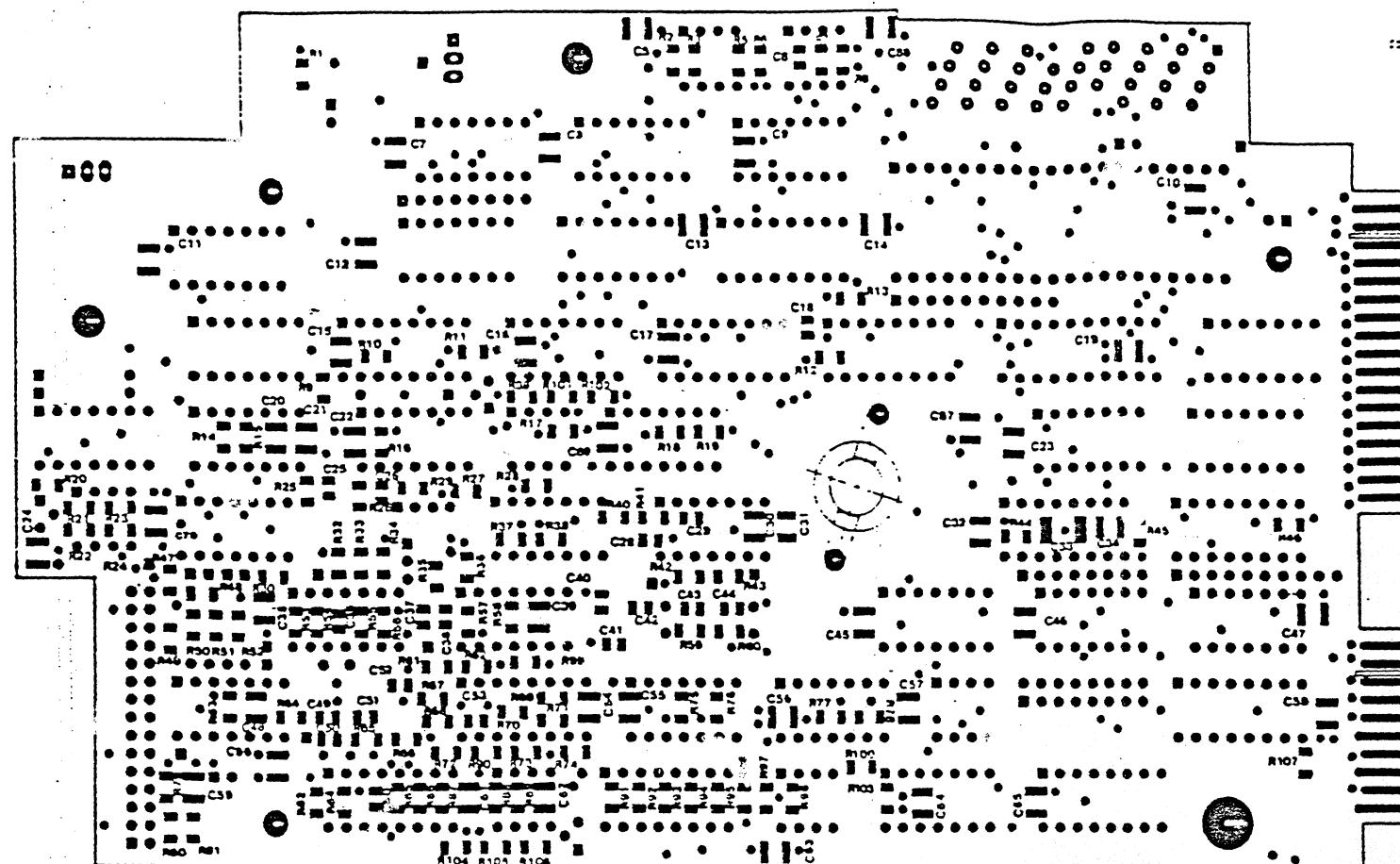


NOTES:

1. SOCKET
2. J12 IS NOT STUFFED, HOLES NOT TO BE SOLDERED.
3. MARK ASSEMBLY NO., REV LETTER AND SERIAL NO. IN PERMANENT INK, APPROX. WHERE SHOWN.
4. TORQUE TO 4 IN-LBS.
5. USE FAB DWG 20081 REV C.
6. A RE-WRAP THESE PINS AFTER FINAL TEST IS COMPLETE
7. REWORK PER REWORK INSTRUCTIONS (P/N 40025).

VIDEOTEX CONTROLLER

QTY	DESC	PART OR IDENTIFICATION NO.	INSTRUCTION NUMBER	MANUFACTURER	MANUFACTURE SPECIFICATION
PARTS LIST					
	UNLESS OTHERWISE SPECIFIED DIMENSIONS ARE IN INCHES TOLERANCES ARE FRACTIONS INCHES AND DECIMALS MILLIMETERS		CONTRACT NO	VERTEX PERIPHERALS	
10170			APPROVALS	PCB ASSEMBLY,	
10150			DATE	DATA, S.C.	
10130	10000		DRAWN	1/14/83	
			CHECKED	1/14/83	
			REVIEWED	1/14/83	
			APPROVED	1/14/83	
			DATE	10041	
			REVISION	1	
			SCALE	NONE	



--- D50 MAX WEIGHT FOR COMPONENTS  
AND LEADS. (EXCEPT ITEM 30308, SCREW)

VERTEX CONFIDENTIAL

ITEM NO.	PRINT OR DRAWING NO.	NAME INCLUTION OR DESCRIPTION	ITEMS SPECIFICATION
PARTS LIST			
UNLESS OTHERWISE SPECIFIED DIMENSIONS ARE IN INCHES TOLERANCES ARE PRACTICAL, DECIMAL, ABSOLUTE		CONTRACT NO.	VERTEX PERIPHERALS
		APPROVALS	DATE
MATERIAL		DRWGS: <u>DC</u>	4/14/83
PRIN		CHG'D: <u>J. French</u>	4/14/83
		RECD'D: <u>DC</u>	4/14/83
DO NOT SCALE DRAWING		ITEM NO.	ITEM NO.
		D	10041
		SCALE: 1/4" = 1'	SPRINT: 4